

**IN THE UNITED STATES DISTRICT COURT  
FOR THE WESTERN DISTRICT OF TEXAS  
AUSTIN DIVISION**

**SheeldS Cyber Ltd.,**  
*Plaintiff*

v.

**NXP USA, Inc.,**  
*Defendant*

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**Case No. 1:23-cv-01169-RP**

**PRELIMINARY CLAIM CONSTRUCTION FOR U.S. PATENT NO. 11,651,088**

The Court provides the following preliminary construction in advance of the *Markman* hearing set for **9 a.m. Friday, October 11, 2024**. The purpose of preliminary construction is to streamline the hearing by providing the parties an indication of the Court’s current position for the terms. The preliminary construction is not final and may be changed based on the arguments at the hearing.

	<b>Disputed Claim Term</b>	<b>Plaintiff’s Proposed Construction</b>	<b>Defendant’s Proposed Construction</b>	<b>Court’s Preliminary Construction</b>
<b>A</b>	<p>“A device for exchanging messages <del>with a second device</del> in a vehicle over a communication bus that consists of, employs, uses, is based on, or is compatible with, a Controller Area Network (CAN)”</p> <p>(Claim 30 and dependent claims)</p>	Plain and ordinary meaning	Indefinite	Plain and ordinary meaning

	Disputed Claim Term	Plaintiff's Proposed Construction	Defendant's Proposed Construction	Court's Preliminary Construction
<b>B</b>	<p>“non-volatile memory”</p> <p>“the memory”</p> <p>(Claims 30, 35, 43)</p>	Plain and ordinary meaning	“semiconductor programmable memory storage that retains data without continuous power”	<p>Plain and ordinary meaning: memory that maintains data when power is removed</p> <p>The parties agree that “the memory” refers to the non-volatile memory</p>
<b>C</b>	<p>“wherein the memory, the first communication interface, and the second communication interface are housed within a single package”</p> <p>(Claim 30)</p>	“a single enclosure, distinct from the enclosure of any ECU in which the device may be integrated, that houses the memory, the first communication interface, and the second communication interface”	Plain and ordinary meaning: the memory and the communications interfaces are in a single component	Plain and ordinary meaning
<b>D</b>	<p>“timing rule” / “a timing rule”</p> <p>(Claims 30, 36)</p>	“a rule that limits the rate that messages can be delivered”	“a rule that limits the rate that messages can be delivered, by buffering the messages and sending them only in preconfigured intervals”	“a rule that limits the rate that messages can be delivered”
<b>E</b>	<p>“timing rule values”</p> <p>“timing information”</p> <p>(Claim 30)</p>	Plain and ordinary meaning (both)	<p>“limits on the message transmission rate to predetermined values per time unit”</p> <p>“an interval after a previous message was transmitted”</p>	Plain and ordinary meaning (both)

	<b>Disputed Claim Term</b>	<b>Plaintiff's Proposed Construction</b>	<b>Defendant's Proposed Construction</b>	<b>Court's Preliminary Construction</b>
<b>F</b>	<p>“to pass, to block, or to change and then pass”</p> <p>“passing, blocking, and changing and then passing”</p> <p>(Claims 30, 36)</p>	Plain and ordinary meaning “passing, blocking, and changing and then passing” refers to Claim 30’s limitation of “to pass, to block, or to change and then pass”	<p>The device must be capable of performing each of the three recited actions on the first message</p> <p>The device must be capable of performing each of the three recited actions on the second message</p>	Plain and ordinary meaning
<b>G</b>	<p>“Electronic Control Unit (ECU)”</p> <p>(Claim 30)</p>	“an electronic system within a vehicle with processing capabilities”	“an addressable end unit capable of serving as source and destination device via a single communication interface”	“an electronic system within a vehicle with processing capabilities”
<b>H</b>	<p>“buffer”</p> <p>(Claim 41)</p>	Plain and ordinary meaning	“memory used temporarily to store one or more messages while they are transferred from an input to an output”	Plain and ordinary meaning
<b>I</b>	<p>“pre-determined time interval”</p> <p>(Claim 42)</p>	Plain and ordinary meaning	Plain and ordinary meaning: a fixed amount of time	Plain and ordinary meaning
<b>J</b>	<p>“Flash-based”</p> <p>(Claim 43)</p>	Plain and ordinary meaning	“an electrically erasable non-volatile memory that uses a floating gate MOSFET as the storage cell”	Plain and ordinary meaning

	Disputed Claim Term	Plaintiff's Proposed Construction	Defendant's Proposed Construction	Court's Preliminary Construction
<b>K</b>	"Universal Asynchronous Receiver Transmitter (UART)"	Plain and ordinary meaning	"a device for asynchronous serial communication that transmits one character as a time as a sequence of 5 to 9 bits, framed by start and stop bits with an optional parity bit"	Plain and ordinary meaning

The Court **ORDERS** that the parties submit any slides they wish to use at the *Markman* hearing to Courtroom Deputy Kieshonda Taylor at Kieshonda\_Taylor@txwd.uscourts.gov, copying opposing counsel, **by 3 p.m. Thursday, October 10, 2024.**

**SIGNED** on October 6, 2024.

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SUSAN HIGHTOWER  
UNITED STATES MAGISTRATE JUDGE